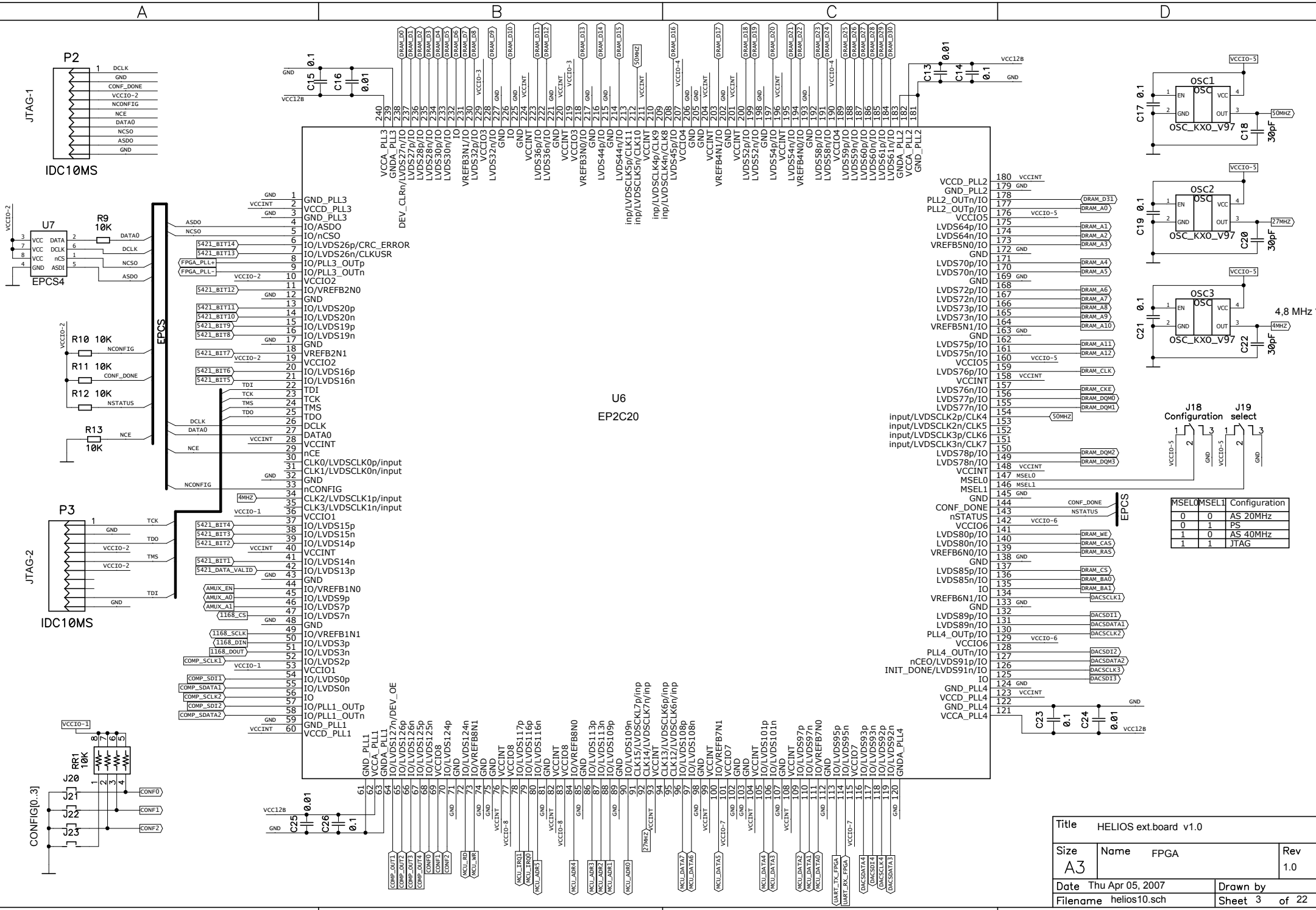
P2
JTAG-1
IDC10MS
DCLK
GND
CONF_DONE
VCCIO-2
NCONFIG
NCE
DATA0
NCSO
ASDO
GND
U7
EPCS4
R9 10K
R10 10K
R11 10K
R12 10K
R13 10K
EPCS
P3
JTAG-2
IDC10MS
U6
EP2C20
GND_PLL3
VCCD_PLL3
GND_PLL3
IO/ASDO
IO/nCSO
IO/LVDS26p/CRC_ERROR
IO/LVDS26n/CLKUSR
IO/PLL3_OUTp
IO/PLL3_OUTn
VCCIO2
IO/VREFB2N0
GND
IO/LVDS20p
IO/LVDS20n
IO/LVDS19p
IO/LVDS19n
GND
VREFB2N1
VCCIO2
IO/LVDS16p
IO/LVDS16n
TDI
TCK
TMS
TDO
DCLK
DATA0
VCCINT
nCE
CLK0/LVDSCLK0p/input
CLK1/LVDSCLK0n/input
GND
nCONFIG
CLK2/LVDSCLK1p/input
CLK3/LVDSCLK1n/input
VCCIO1
IO/LVDS15p
IO/LVDS15n
IO/LVDS14p
VCCINT
IO/LVDS14n
IO/LVDS13p
GND
IO/VREFB1N0
IO/LVDS9p
IO/LVDS7p
IO/LVDS7n
GND
IO/VREFB1N1
IO/LVDS3p
IO/LVDS3n
IO/LVDS2p
VCCIO1
IO/LVDS0p
IO/LVDS0n
IO
IO/PLL1_OUTp
IO/PLL1_OUTn
GND_PLL1
VCCD_PLL1
GND_PLL1
VCCA_PLL1
GNDA_PLL1
IO/LVDS127n/DEV_OE
IO/LVDS126p
IO/LVDS126n
IO/LVDS125p
IO/LVDS125n
VCCIO8
IO/LVDS124p
GND
IO/LVDS124n
IO/VREFB8N1
GND
GND
VCCINT
VCCIO8
IO/LVDS117p
IO/LVDS116p
IO/LVDS116n
GND
VCCIO8
IO/VREFB8N0
GND
IO/LVDS113p
IO/LVDS113n
IO/LVDS109p
GND
IO/LVDS109n
CLK15/LVDSCKL7p/inp
CLK14/LVDSCLK7n/inp
VCCINT
CLK13/LVDSCLK6p/inp
CLK12/LVDSCLK6n/inp
IO/LVDS108p
IO/LVDS108n
GND
VCCINT
IO/VREFB7N1
VCCIO7
GND
VCCINT
IO/LVDS101p
IO/LVDS101n
GND
VCCINT
IO/LVDS97p
IO/LVDS97n
IO/VREFB7N0
GND
IO/LVDS95p
IO/LVDS95n
VCCIO7
IO/LVDS93p
IO/LVDS93n
IO/LVDS92p
IO/LVDS92n
GNDA_PLL4
VCCA_PLL4
GND_PLL4
VCCD_PLL4
GND_PLL4
IO
INIT_DONE/LVDS91n/IO
nCEO/LVDS91p/IO
PLL4_OUTn/IO
VCCIO6
PLL4_OUTp/IO
LVDS89n/IO
LVDS89p/IO
VCCIO6
VREFB6N0/IO
LVDS80n/IO
LVDS80p/IO
VCCIO6
nSTATUS
CONF_DONE
GND
VCCINT
LVDS78n/IO
LVDS78p/IO
input/LVDSCLK3n/CLK7
input/LVDSCLK3p/CLK6
input/LVDSCLK2n/CLK5
input/LVDSCLK2p/CLK4
LVDS77n/IO
LVDS77p/IO
LVDS76n/IO
VCCINT
LVDS76p/IO
VCCIO5
LVDS75n/IO
LVDS75p/IO
GND
LVDS73n/IO
LVDS73p/IO
LVDS72n/IO
LVDS72p/IO
GND
LVDS70n/IO
LVDS70p/IO
GND
VREFB5N1/IO
LVDS73n/IO
LVDS73p/IO
LVDS72n/IO
LVDS72p/IO
GND
VREFB5N0/IO
LVDS64n/IO
LVDS64p/IO
VCCIO5
PLL2_OUTp/IO
PLL2_OUTn/IO
GND_PLL2
VCCD_PLL2
GND_PLL2
VCCA_PLL2
GNDA_PLL2
DEV_CLRn/LVDS27n/IO
LVDS27p/IO
LVDS28n/IO
LVDS28p/IO
LVDS30p/IO
LVDS30n/IO
IO
VREFB3N1/IO
LVDS32p/IO
VCCIO3
LVDS32n/IO
GND
IO
GND
VCCINT
LVDS36p/IO
LVDS36n/IO
GND
VCCINT
VCCIO3
VREFB3N0/IO
GND
LVDS44p/IO
GND
LVDS44n/IO
inp/LVDSCLK5p/CLK11
inp/LVDSCLK5n/CLK10
VCCINT
inp/LVDSCLK4p/CLK9
inp/LVDSCLK4n/CLK8
LVDS45p/IO
VCCIO4
GND
GND
VCCINT
VREFB4N1/IO
GND
VCCINT
LVDS52p/IO
LVDS52n/IO
GND
LVDS54p/IO
VCCINT
LVDS54n/IO
VREFB4N0/IO
GND
LVDS58p/IO
LVDS58n/IO
VCCIO4
LVDS59p/IO
LVDS59n/IO
VCCIO4
LVDS60p/IO
LVDS60n/IO
LVDS61p/IO
LVDS61n/IO
GNDA_PLL2
VCCA_PLL2
GND_PLL2
DRAM_D0
DRAM_D1
DRAM_D2
DRAM_D3
DRAM_D4
DRAM_D5
DRAM_D6
DRAM_D7
DRAM_D8
DRAM_D9
DRAM_D10
DRAM_D11
DRAM_D12
DRAM_D13
DRAM_D14
DRAM_D15
50MHZ
DRAM_D16
DRAM_D17
DRAM_D18
DRAM_D19
DRAM_D20
DRAM_D21
DRAM_D22
DRAM_D23
DRAM_D24
DRAM_D25
DRAM_D26
DRAM_D27
DRAM_D28
DRAM_D29
DRAM_D30
DRAM_D31
DRAM_A0
DRAM_A1
DRAM_A2
DRAM_A3
DRAM_A4
DRAM_A5
DRAM_A6
DRAM_A7
DRAM_A8
DRAM_A9
DRAM_A10
DRAM_A11
DRAM_A12
DRAM_CLK
DRAM_CKE
DRAM_DQM0
DRAM_DQM1
DRAM_DQM2
DRAM_DQM3
DRAM_WE
DRAM_CAS
DRAM_RAS
DRAM_CS
DRAM_BA0
DRAM_BA1
DACSCLK1
DACSDI1
DACSDATA1
DACSCLK2
DACSDI2
DACSDATA2
DACSCLK3
DACSDI3
C13 0.01
C14 0.1
C15 0.1
C16 0.01
C23 0.1
C24 0.01
C25 0.01
C26 0.1
VCC12B
GND
OSC1
OSC2
OSC3
OSC_KX0_V97
C17 0.1
C18 30pF
C19 0.1
C20 30pF
C21 0.1
C22 30pF
4,8 MHz ???
J18 Configuration
J19 select
MSEL0 MSEL1 Configuration
0 0 AS 20MHz
0 1 PS
1 0 AS 40MHz
1 1 JTAG
RR1 10K
J20
J21
J22
J23
CONFIG[0..3]
CONF0
CONF1
CONF2
Title HELIOS ext.board v1.0
Size A3
Name FPGA
Rev 1.0
Date Thu Apr 05, 2007
Drawn by
Filename helios10.sch
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